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The eProcessor project RISC-V ecosystem

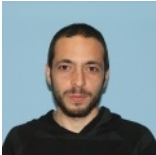
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Abstract

eProcessor is a 3-year EuroHPC-JU project (started on April 2021) that involves 10 academic/industrial partners, which aims to provide an open source European RISC-V based out-of-order processor core, accelerators and a HW/SW stack, tailored for HPC, Bioinformatics and AI applications.

In this talk, the eProcessor project and its objectives will be introduced, and a brief overview of other European efforts in the RISC-V and open hardware movements will be given.

Biography



Dr. Nehir Sonmez holds a PhD in Computer Engineering (2012) from the Technical University of Catalonia (UPC), Spain, an MS degree from Bogazici University, Turkey (2006) and a BS degree from Syracuse University, USA (2003). He is currently a senior researcher in the High Performance Domain-Specific Architectures research group at BSC, where he is the coordinator of the EuroHPC-JU eProcessor project. He is also active in the architecture design and verification efforts in EPI and DRAC projects, on RISC-V processors and vector accelerators. His other research interests include reconfigurable computing, computer architecture and multicores, transactional memory, disaggregated computing, and database acceleration.

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