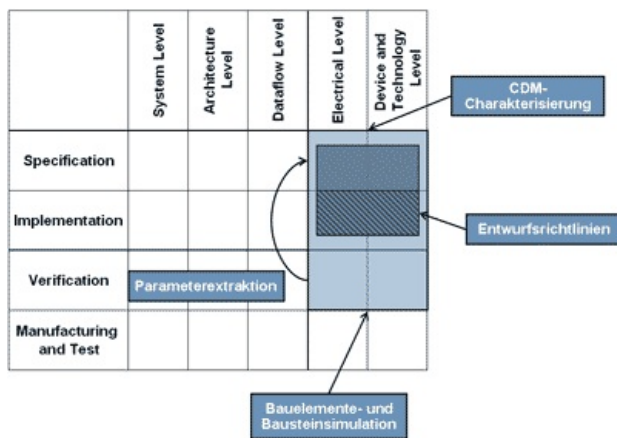


SIDRA: Safe IC-Design for Robust Application



Robust operation of complex power and mixed analogue/digital devices in harsh environments is essential for the electronic systems deployed increasingly to monitor vital control and safety functions in modern motor vehicles. Similar reliability standards are necessary in many other applications, ranging from security to communications and even some consumer appliances. A particularly important requirement is to ensure that such components are resistant to damage caused by electrostatic discharge (ESD). The project SIDRA is responding to this need by developing simulation-guided design methods that for the first time will aim to address ESD protection at a whole-chip level. ESD occurs during production as well as later in use and is a potential "chip killer". The project is processed on national and European level (MEDEA+ T104). The effects of the project work will be ESD protection of ICs in future technologies, i.e. in safety critical applications like automotive, identification systems and chip cards, the risk minimization of expensive and image damaging problems in the field and the achievability of specification demands, which are not possible today due to a matter of robustness.

Classification in the edaMatrix:



Project coordination:

Robert Bosch GmbH
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 fon:

Project partners:

- [Atmel Germany GmbH](#)
- [Infineon Technologies AG](#)
- [Robert Bosch GmbH](#)
- [X-FAB Global Services GmbH](#)

Funding initial:

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Final Report
 NL 03 2007 (PSB)
 NL 01 2007 (PN)
 NL 04 2006 (PN)
 NL 04 2006 (PB)
 NL 01 2006 (PN)
 NL 02 2005 (PN)
 NL 04 2004 (PN)

Runtime:

Sun, 01 August 2004 - Sun, 31 December 2006

Website:

Used Abbreviations

Abbreviation	Meaning
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PR	Project Report
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SPR	Short Project Report
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PN	Project News
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FPR	Final Project Report
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Source URL: <https://www.edacentrum.de/en/projects/SIDRA>