

# Submission Page for the 5th Workshop on RISC-V Activities

Verfasst von Andreas Vörg, edacentrum GmbH, DE am Di, 2021/05/18 - 10:21



## 5<sup>th</sup> Workshop on RISC-V Activities 7 November 2022

This joint academic/industry workshop aims to stimulate the exchange of information among the attendees about already existing or planned RISC-V activities. The workshop provides a platform for how these activities can be extended across projects or to develop new ideas, activities and collaborations. This workshop has been initiated by the BMBF funded project Scale4Edge <sup>[1]</sup> and will be executed in conjunction with the edaForum22 and MICROELECTRONICS FOR FUTURE 22 <sup>[2]</sup>.

**Date:** 7 November 2022

**Location:** Hotel NH Collection Berlin Mitte, Friedrichstraße 96, 10117 Berlin, Germany <sup>[3]</sup>; in Google Maps <sup>[4]</sup>

**Workshop language:** English

RISC-V is one of the hottest trends in the industry these days, with its mature software toolchain and many hardware processor providers offering implementations ranging from textbook open-source cores to high-end commercial ones. The freedom to configure and customize the RISC-V ISA in accordance to the system needs, including custom instructions, is one of its strongest appeals, making custom RISC-V CPUs an attractive choice for an unprecedented number of companies. However, the challenge of actually designing a RISC-V core with custom extensions and ensuring its correct functional behaviour is still significant, even more in environments with high safety and security expectations.

**About the Workshop series:** <https://www.edacentrum.de/en/risc-v/trainings> <sup>[5]</sup>

**Program:** <https://www.edacentrum.de/en/risc-v/program> <sup>[6]</sup>

### Registration

- Registration deadline: 31 October 2022 AoE\*)  
Registration is open!  
Registrations can be done online at <https://www.edacentrum.de/en/risc-v/registration> <sup>[7]</sup> only!

### Deadlines

Short abstract deadline: Sep 28, 2022 AoE<sup>\*)</sup>

Author notification: Oct 7, 2022 AoE<sup>\*)</sup>

Program available: Oct 14, 2022 AoE<sup>\*)</sup>

Registration deadline: Oct 31, 2022 AoE<sup>\*)</sup>

<sup>\*)</sup> AoE = Anywhere on Earth

## Organizing Committee

- Oliver Bringmann, Universität Tübingen, DE
- Wolfgang Ecker, Infineon Technologies, DE
- Andreas Mauderer, Robert Bosch GmbH, DE
- Daniel Müller-Gritschneider, Technische Universität München, DE
- Wolfgang Müller, Universität Paderborn, DE
- Dieter Treytnar, edacentrum, DE
- Andreas Vörg, edacentrum, DE
- Stefan Wallentowitz, Hochschule München, DE

In case of questions, please contact:

Andreas Vörg or Dieter Treytnar

risc-v@edacentrum [dot] de

## Submission Page is closed!

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**Quell-URL:** <https://www.edacentrum.de/risc-v>

### Links:

[1] <https://www.edacentrum.de/scale4edge/en>

[2] <https://www.microelectronics4future.com/de>

[3] <https://www.nh-hotels.com/hotel/nh-collection-berlin-mitte-friedrichstrasse>

[4] <https://g.page/NHCollectionFriedrichstrasse?share>

[5] <https://www.edacentrum.de/en/risc-v/trainings>

[6] <https://www.edacentrum.de/en/risc-v/program>

[7] <https://www.edacentrum.de/en/risc-v/registration>