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System Verification: THE Enterprise Level Development Problem

edaForum06 Presentation

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Abstract

With the evolution of System within Systems, there has been a lot of talk about the importance of ESL tools for system modeling and early embedded software development. It is true that these areas are important. However, in order for new approaches to deliver the real results for project teams, they must be integrated into the broader enterprise wide process that starts with specification and verification planning, includes block and chip level development and verification, and ultimately full system validation and closure. This process involves multiple specialists, including the systems, software, logic design, verification, and system validation engineers, and the project and verification managers that must drive and orchestrate the enterprise wide process to closure. The presentation will describe how companies need to completely re-think and re-engineer their development and verification management approach with an enterprise wide view.

Biography



Moshe Gavrielov Executive Vice President and General Manager Verification Division Cadence Design Systems, Inc.

Moshe Gavrielov serves as Cadence Executive Vice President and General Manager, reporting to Michael J. Fister, President and CEO. He is responsible for the company's Verification Division.

He has over 25 years of technology and business management experience, including serving as CEO of Verisity, Ltd. before joining Cadence in April 2005. Prior to joining Verisity, he worked nine years at LSI Logic, where he held several executive management positions. Those positions included executive vice president of the products organization, senior vice president of international markets, general manager of LSI Logic Europe and general manager of the ASIC division.

He received his Bachelor of Science degree in electrical engineering and a Masters degree in computer science from the Israel Institute of Technology (Technion) in Haifa, Israel.

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